

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Meikei Jeong, et al.

Examiner: Unassigned

Serial No: Unassigned

Art Unit: Unassigned

Filed: Herewith

Docket: FIS920010324US2 (15093A)

For: METHOD AND STRUCTURE OF A
DISPOSABLE REVERSED SPACER
PROCESS FOR HIGH PERFORMANCE
RECESSED CHANNEL CMOS

Date: November 14, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

In connection with filing the above-identified application under 37 C.F.R. §1.53(b), applicants submit the following amendments and remarks for consideration by the Examiner and entry of record in the above-identified patent application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 5 of this paper.

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Date of Deposit: November 14, 2003

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Dated: November 14, 2003



Leslie S. Szivos